IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re the Application of:

SUNDEEP M. BAJIKAR, ET AL.

Application No.: 10/643,678

Filed: August 18, 2003

For: **Low Pin Count Docking Architecture for a Trusted Platform**

Art Group: 2139

Examiner: Kent Williams

Confirmation No.: 4611

INFORMATION DISCLOSURE STATEMENT UNDER 37 C.F.R. §1.97

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

In accordance with the duty of disclosure, enclosed is a copy of IDS Citation Form PTO/SB/08 or PTO-1449, together with copies of the documents cited on that form, except for copies not required to be submitted (e.g., copies of U.S. patents and U.S. published patent applications need not be enclosed). This IDS and IDS Citation Form are being submitted before the mailing of a final Office Action. It is respectfully requested that the cited references be considered and that the enclosed copy of PTO/SB/08 be initialed by the Examiner to indicate such consideration and a copy thereof returned to applicant(s).
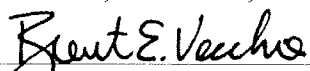
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The fee set in the amount of $180.00 for submission of the Information Disclosure Statement is enclosed herewith. Please charge any additional fees due to Deposit Account 02-2666. A duplicate copy of the Fee Transmittal (PTO/SB/17) is enclosed for this purpose.

Respectfully submitted,

BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN LLP

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I hereby certify that this correspondence is being submitted electronically via EFS Web on the date shown below.

Wendi Lou Rostan   *06-26-07*

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INFORMATION DISCLOSURE STATEMENT BY APPLICANT
(Use as many sheets as necessary)

Complete if Known	
Application Number	10/643,678
Filing Date	August 18, 2003
First Named Inventor	Bajikar, Sundeep
Group Art Unit	2139
Examiner Name	Williams, Kent

Sheet 1 of 2

Attorney Docket No: 42P16632

OTHER DOCUMENTS -- NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No [1]	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T[2]
		CRAWFORD, JOHN , "Architecture of the Intel 80386", Proceedings of the IEEE International Conference on Computer Design: VLSI in Computers and Processors (ICCD '86), (October 6, 1986),155-160	
		DAVIDA, GEORGE I., et al., "Defending Systems Against Viruses through Cryptographic Authentication", Proceedings of the Symposium on Security and Privacy, IEEE Comp. Soc. Press, ISBN 0-8186-1939-2,(May 1989),	
		FABRY, R.S. , "Capability-Based Addressing", Fabry, R.S., "Capability-Based Addressing," Communications of the ACM, Vol. 17, No. 7, (July 1974),403-412	
		FRIEDER, GIDEON , "The Architecture And Operational Characteristics of the VMX Host Machine", The Architecture And Operational Characteristics of the VMX Host Machine, IEEE, (1982),9-16	
		GOLDBERG, ROBERT P., "Survey of Virtual Machine Research", COMPUTER Magazine, (06/1974),34-35	
		GONG, LI , et al., "Going Behond the Sandbox: An Overview of the New Security Architecture in the Java Development Kit 1.2", Proceedings of the USENIX Symposium on Internet Technologies and Systems, Monterey, CA,(12/1997),	
		GUM, P. H., "System/370 Extended Architecture: Facilities for Virtual Machines", IBM J. Research Development, Vol 27, Number 6, (11/1983),530-544	
		HALL, JUDITH S., et al., "Virtualizing the VAX Architecture", ACM SIGARCH Computer Architecture News, Proceedings of the 18th Annual International Symposium on Computer Architecture, Vol. 19, Issue No. 3, (April 1991),10 pages	
		HEINRICH, JOE , "MIPS R4000 Microprocessor User's Manual, Second Edition", Chapter 4 "Memory Management", (6/11/1993),61-97	
		HP MOBILE SECURITY OVERVIEW, "HP Mobile Security Overview", (Sept. 2002),1-10	
		IBM, "Information Display Technique for a Terminate Stay Resident Program IBM Technical Disclosure Bulletin", TDB-ACC-No. NA9112156, Vol. 34, Issue 7A, (12/1/1991),156-158	
		IBM CORPORATION, "IBM ThinkPad T30 Notebooks", IBM Product Specification, located at www-1.ibm.com/services/files/cisco_t30_spec_sheet_070202.pdf, last visited June 23, 2004,(July 2, 2002),1-6	
		INTEL CORPORATION, "IA-32 Intel Architecture Software Developer's Manual", Volume 3: System Programming Guide, Intel Corporation - 2003,13-1 through 13-24	
		INTEL CORPORATION, "IA-64 System Abstraction Layer Specification", Intel Product Specification, Order Number 245359-001, (01/2000),1-112	
		INTEL CORPORATION, "Intel 82802AB/82802AC Firmware Hub (FWH)", Intel Product Datasheet, Document Number 290658-004,(November 2000),1-6, 17-28	
		INTEL CORPORATION, "Intel IA-64 Architecture Software Developer's Manual", Volume 2: IA-64 System Architecture, Order Number 245318-001, (01/2000),i, ii, 5.1-5.3, 11.1-11.8, 11.23-11.26	

EXAMINER   **DATE CONSIDERED**

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OTHER DOCUMENTS -- NON PATENT LITERATURE DOCUMENTS

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		INTEL CORPORATION, "Intel386 DX Microprocessor 32-Bit CHMOS Microprocessor With Integrated Memory Management", (1995),5-56	
		KARGER, PAUL A., et al., "A VMM Security Kernal for the VAX Architecture", Proceedings of the Symposium on Research in Security and Privacy, XP010020182, ISBN 0-8186-2060-9, Boxborough, MA, (5/7/1990),2-19	
		KASHIWAGI, KAZUHIKO , et al., "Design and Implementation of Dynamically Reconstructing System Software", Software Engineering Conference, Proceedings 1996 Asia-Pacific Seoul, South Korea 4-7 Dec. 1996, Los Alamitos, CA USA, IEEE Comput. Soc, US, ISBN 0-8186-7638-8,(1996),	

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